

LPDDR测试板保留接口

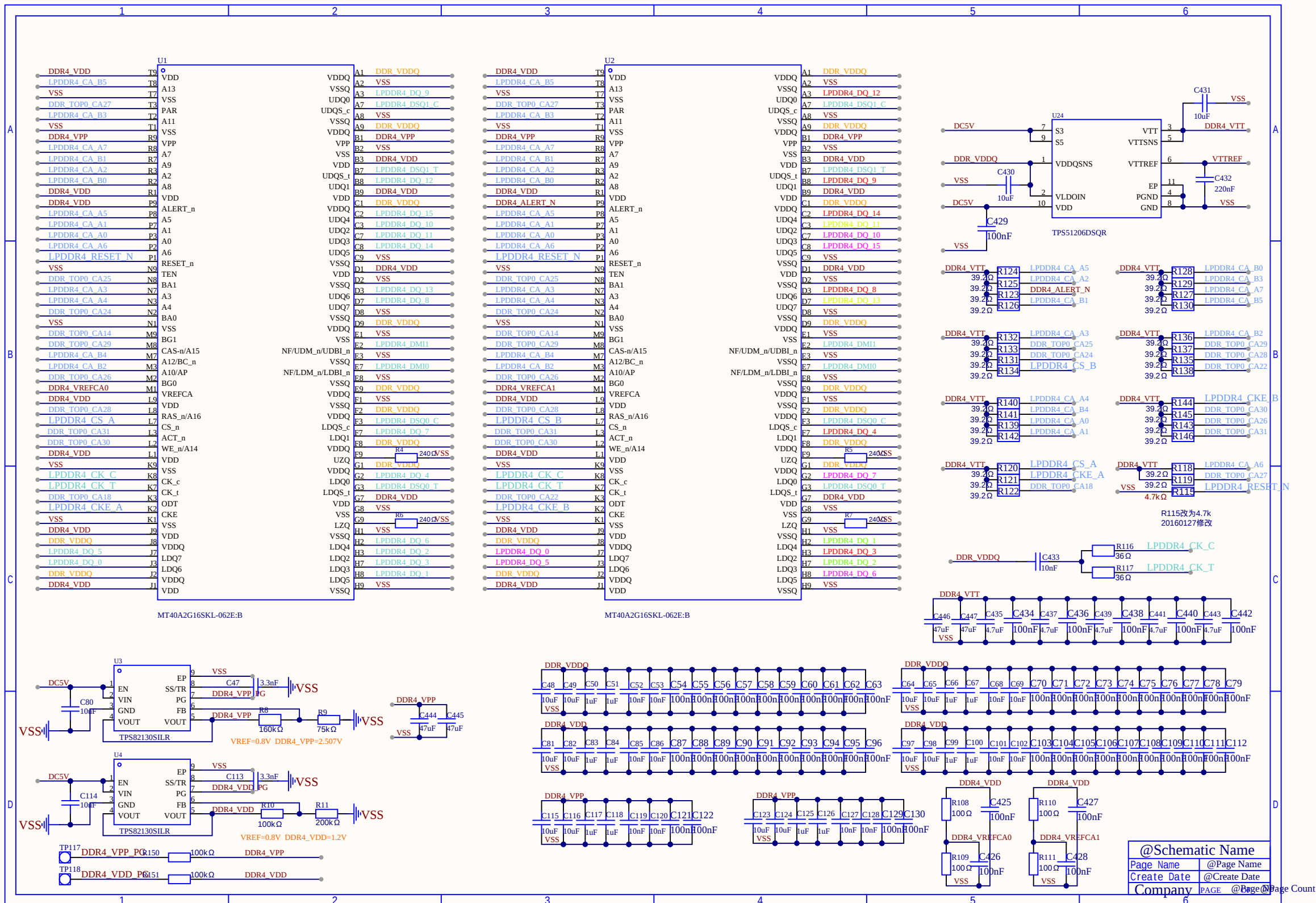
- 1 : SD
- 2 : FLASH
- 3 : EMMC
- 4 : ETH1000M
- 5 : JTAG
- 6 : USB

DDR测试板增加接口

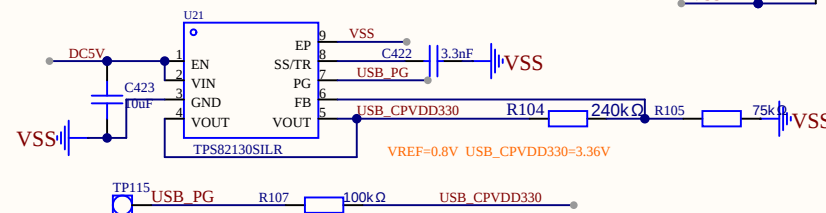
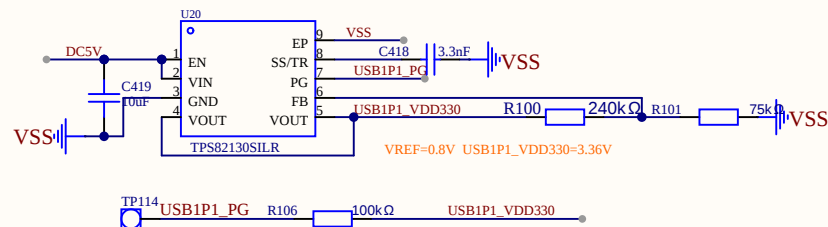
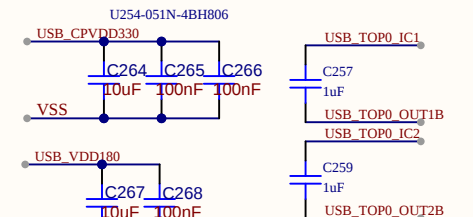
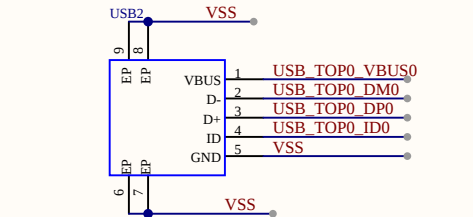
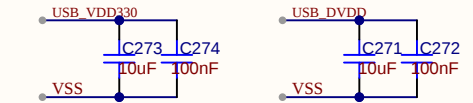
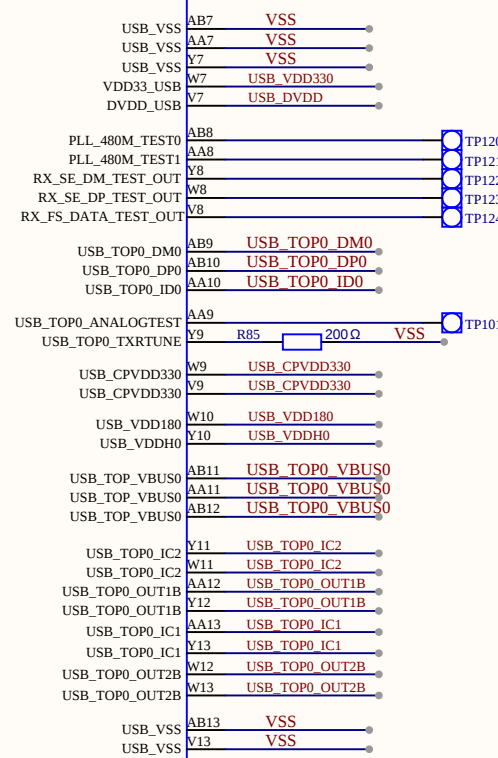
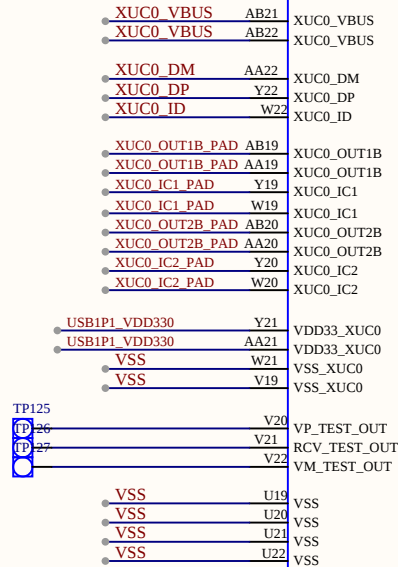
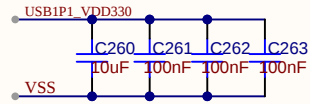
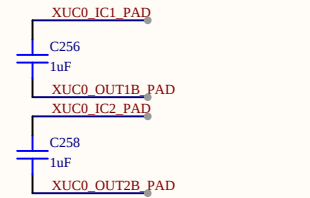
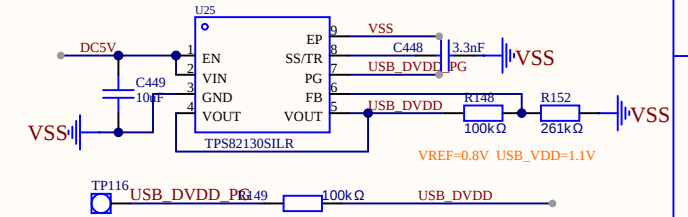
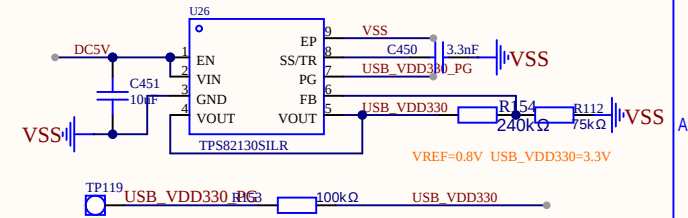
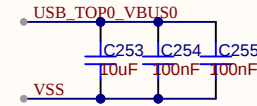
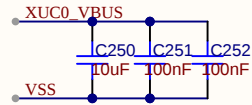
- 1 : ADC
- 2 : PWM
- 3 : DMA
- 4 : RS485---usart
- 5 : SMART卡----usart
- 6 : 触摸按键
- 7 : comp
- 8 : dcmi
- 9 : dfsdm
- 10 : 温度传感器--i2c
- 11 : spdifrx
- 12 : VREF
- 13 : DDR4
- 14 : pmu
- 15 : 陀螺仪---i3c
- 16 : hs_sdio/i3c电压可控
- 17 : 小数时钟发生器--i2c
- 18 : SPI屏
- 19 : SDIO SLAVE测试---HS_SDIO接SDIO??

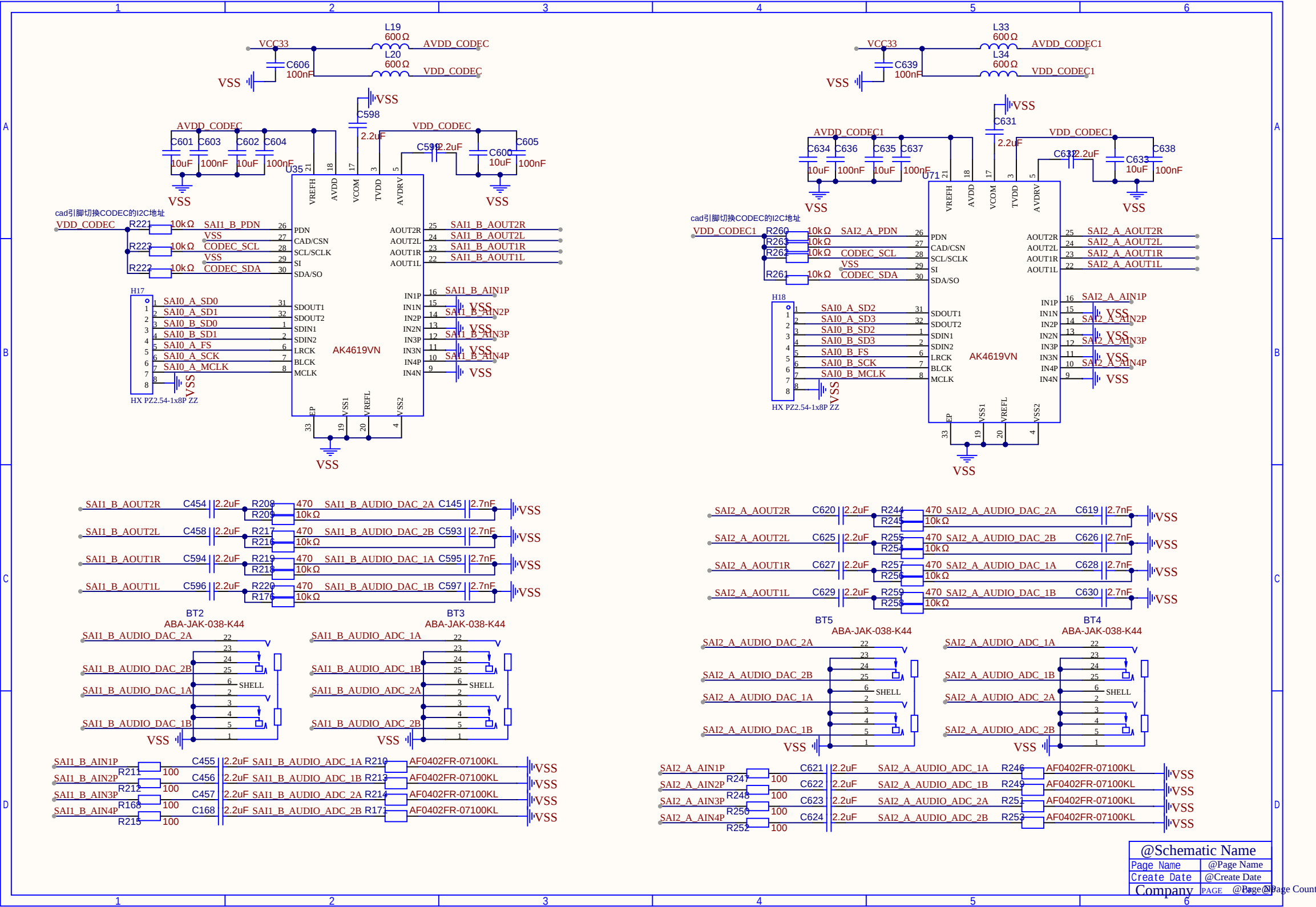
修改记录

- 1 : 修改SIM卡时钟来源为USART5_SCLK



U254-051N-4BH806





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Page Name @Page Name

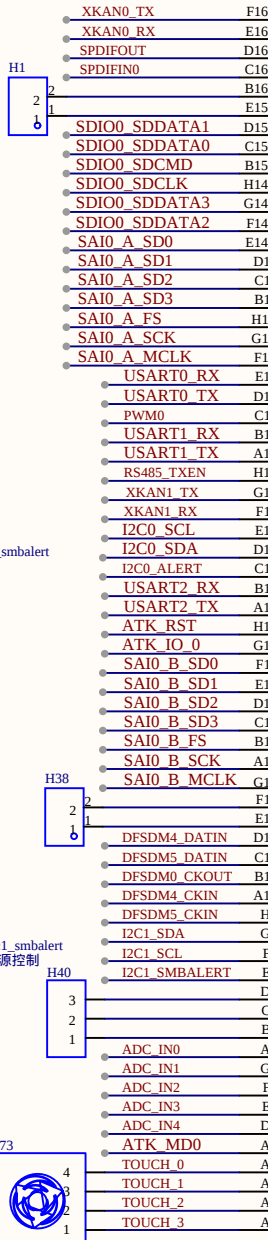
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GPIO1/XEC

VDDIO0供电与IO低速功能分组0：0-21
VDDIO1供电与IO低速功能分组1：22-42
VDDIO2供电与IO低速功能分组2：43-63

U27.2



P0_TESTCHIP_BGA484

IOMUX_0	QSPI_XIP0_CS	GMC0_DQ_27	DBGMUX0
IOMUX_1	QSPI_XIP0_DQ1	GMC0_DQ_26	DBGMUX1
IOMUX_2	QSPI_XIP0_DQ2	GMC0_DQ_25	DBGMUX2
IOMUX_3	QSPI_XIP0_DQ3	GMC0_DQ_24	DBGMUX3
IOMUX_4	QSPI_XIP0_SCK	GMC0_DQ_23	DBGMUX4
IOMUX_5	QSPI_XIP0_DQ0	GMC0_DQ_22	DBGMUX5
IOMUX_6	SDIO0_SDDATA1	GMC0_DQ_21	XEC_GEN20_GMII_TXD3
IOMUX_7	SDIO0_SDDATA0	GMC0_DQ_20	DBGMUX6
IOMUX_8	SDIO0_SDCMD	GMC0_DQ_19	DBGMUX7
IOMUX_9	SDIO0_SCLK	GMC0_DQ_18	DBGMUX8
IOMUX_10	SDIO0_SDDATA3	GMC0_DQ_17	XEC_GEN20_GMII_TXD2
IOMUX_11	SDIO0_SDDATA2	GMC0_DQ_16	XEC_GEN20_GMII_RXD3
IOMUX_12	SDIO0_SDDATA4	GMC0_SDR_CKE1	XEC_GEN20_GMII_RXD2
IOMUX_13	SDIO0_SDDATA5	GMC0_SDR_CSN1	XEC_GEN20_GMII_COL
IOMUX_14	SDIO0_SDDATA6	GMC0_NPC_NBL2	XEC_GEN20_GMII_CRS
IOMUX_15	SDIO0_SDDATA7	GMC0_NPC_NBL3	XEC_GEN20_GMII_TXER
IOMUX_16	XEC_GEN20_MDIO	GMC0_ADDR_23	QSPI_XIP0_CS
IOMUX_17	XEC_GEN20_MDC	GMC0_ADDR_22	QSPI_XIP0_DQ1
IOMUX_18	XEC_GEN20_GMII_TXEN	GMC0_ADDR_21	QSPI_XIP0_DQ2
IOMUX_19		GMC0_ADDR_15	DBGMUX9
IOMUX_20		GMC0_ADDR_14	DBGMUX10
IOMUX_21	XEC_GEN20_GMII_TXD3	GMC0_ADDR_13	DBGMUX11
IOMUX_22	XEC_GEN20_GMII_TXD2	GMC0_ADDR_12	QSPI_XIP0_DQ3
IOMUX_23	XEC_GEN20_GMII_TXD1	GMC0_ADDR_19	QSPI_XIP0_SCK
IOMUX_24	XEC_GEN20_GMII_TXD0	GMC0_ADDR_20	QSPI_XIP0_DQ0
IOMUX_25	XEC_GEN20_GMII_TXC	QSPI_XIP1_DQ7	DBGMUX12
IOMUX_26	XEC_GEN20_GMII_RXC	QSPI_XIP1_DQ6	DBGMUX13
IOMUX_27	XEC_GEN20_GMII_RXD3	GMC0_ADDR_11	DBGMUX14
IOMUX_28	XEC_GEN20_GMII_RXD2	GMC0_ADDR_10	DBGMUX15
IOMUX_29	XEC_GEN20_GMII_RXD1	QSPI_XIP1_DQ5	DBGMUX16
IOMUX_30	XEC_GEN20_GMII_RXD0	QSPI_XIP1_DQ4	DBGMUX17
IOMUX_31	XEC_GEN20_GMII_RXDV	QSPI_XIP1_DQ2	DBGMUX18
IOMUX_32	XEC_GEN20_GMII_RXER	QSPI_XIP1_DQ3	DBGMUX19
IOMUX_33	XEC_GEN20_GMII_COL	GMC0_ADDR_17	
IOMUX_34	XEC_GEN20_GMII_CRS	GMC0_ADDR_16	
IOMUX_35	XEC_GEN20_GMII_TXER	GMC0_ADDR_9	
IOMUX_36	XEC_GEN20_PPS_OUT	GMC0_ADDR_8	
IOMUX_37		GMC0_ADDR_7	
IOMUX_38		GMC0_ADDR_6	
IOMUX_39	QSPI_XIP1_DQS	GMC0_ADDR_5	
IOMUX_40	QSPI_XIP1_DQ5	GMC0_ADDR_4	
IOMUX_41	QSPI_XIP1_DQ6	GMC0_ADDR_3	
IOMUX_42	QSPI_XIP1_DQ7	GMC0_ADDR_2	
IOMUX_43	QSPI_XIP1_DQ2	GMC0_ADDR_1	
IOMUX_44	QSPI_XIP1_DQ3	GMC0_ADDR_0	
IOMUX_45	QSPI_XIP1_CS	SDIO0_SDDATA4	
IOMUX_46	QSPI_XIP1_DQ1	SDIO0_SDDATA5	
IOMUX_47	QSPI_XIP1_SCK	SDIO0_SDDATA6	
IOMUX_48	QSPI_XIP1_DQ0	SDIO0_SDDATA7	
IOMUX_49			PID0_EPPO_RD_READY
IOMUX_50			PID0_LED_WORKING
IOMUX_51			PID0_EPPI_SCK_M
IOMUX_52			PID0_EPPI_VLD
IOMUX_53			PID0_EPPI_CSN0
IOMUX_54			PID0_EPPI_CSN1
IOMUX_55			PID0_EPPI_CSN2
IOMUX_56			PID0_EPPI_SDA0
IOMUX_57			PID0_EPPI_SDA1
IOMUX_58			PID0_EPPI_SDA2
IOMUX_59			PID0_EPPI_SDA3
IOMUX_60			PID0_EPPI_SDA4
IOMUX_61			PID0_EPPI_SDA5
IOMUX_62			PID0_EPPI_SDA6
IOMUX_63			PID0_EPPI_SDA7

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Page Name @Page Name

Create Date @Create Date

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Page Count

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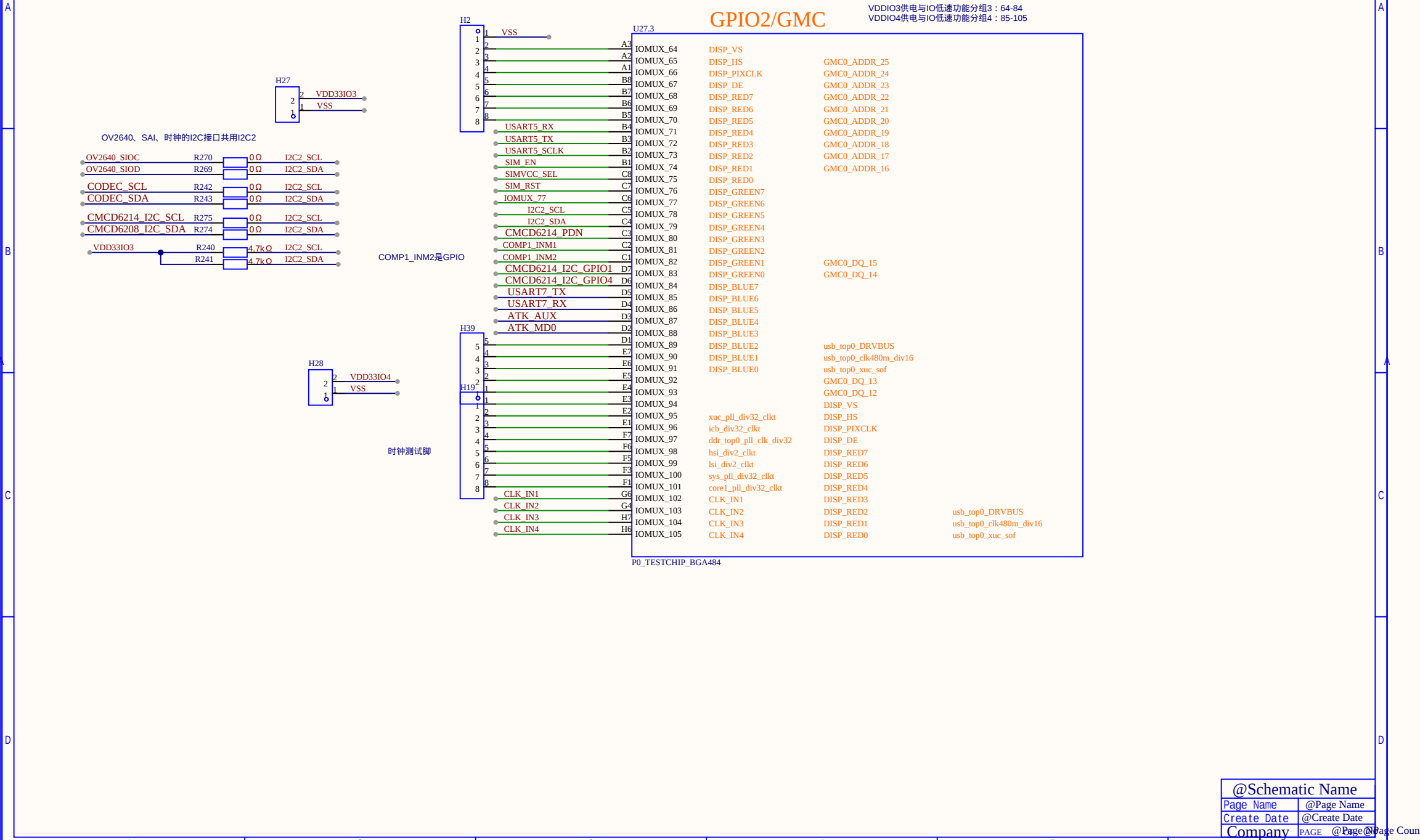
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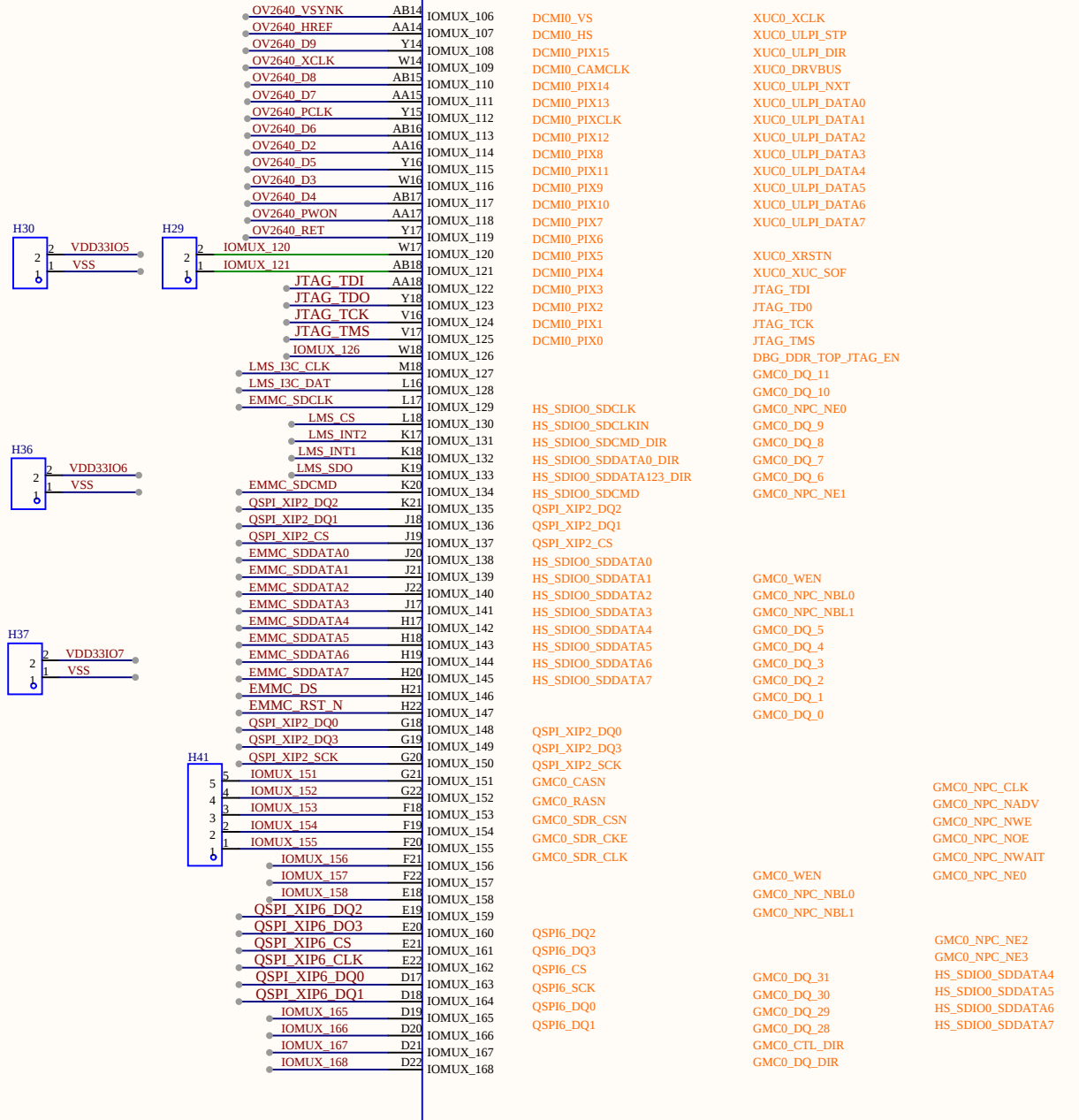


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Company	PAGE @Page@Page Count

U27.4

GPIO2/GMC

VDDIO5供电与IO低速功能分组5：106-126
VDDIO6供电与IO低速功能分组6：127-147
QSPI_XIP2属于FLASH电源供电
VDDIO7供电与IO低速功能分组7：148-168



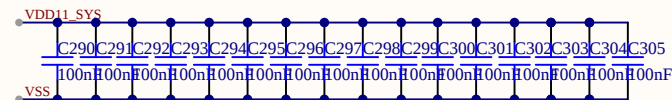
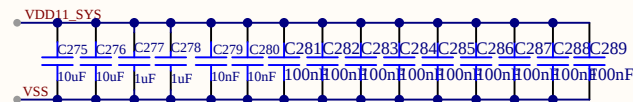
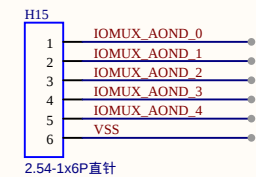
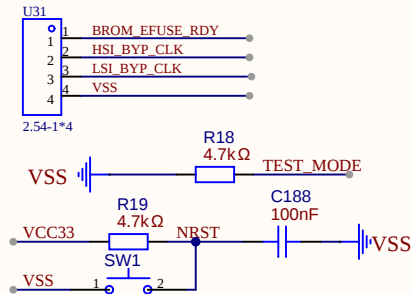
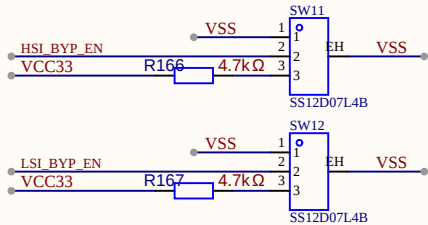
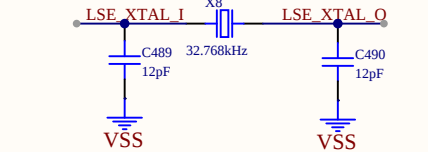
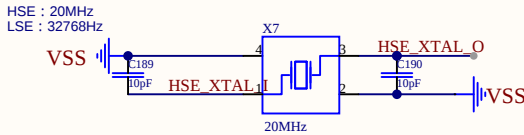
P0_TESTCHIP_BGA484

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Page Name	@Page Name
Create Date	@Create Date
Company	PAGE @Page@Page Count

POWER

3.3V

1.1V



VDD33_PLL	M20	VDD33_PLL
VSS	M19	VSS
VSS	M21	VSS
VSS	M22	VSS
HSI_BYP_EN	L19	HSI_BYP_EN
BROM_EFUSE_RDY	L20	BROM_EFUSE_RDY
HSE_XTAL_O	L21	HSE_XTAL_O
HSE_XTAL_I	L22	HSE_XTAL_I
HSI_BYP_CLK	K22	HSI_BYP_CLK
VDD33_AOND_PSO	A14	VDD33_AOND_PSO
VDD33_AOND_PSO	A15	VDD33_AOND_PSO
VSS	A16	VSS
VDD33_AOND_PSI	A17	VDD33_AOND_PSI
VDD33_AOND_PSI	B17	VDD33_AOND_PSI
VSS	C18	VSS
VDD33_AOND_BAT	C19	VDD33_AOND_BAT
PUDC_V33_BYP	A18	PUDC_V33_BYP
IOMUX_AOND_0	B18	IOMUX_AOND_0
IOMUX_AOND_1	B19	IOMUX_AOND_1
IOMUX_AOND_2	B20	IOMUX_AOND_2
IOMUX_AOND_3	B21	IOMUX_AOND_3
IOMUX_AOND_4	B22	IOMUX_AOND_4
TEST_MODE	C20	TEST_MODE
NRST	C21	NRST
LDO_1A_BYP	C22	LDO_1A_BYP
LSE_XTAL_I	A19	LSE_XTAL_I
LSE_XTAL_O	A20	LSE_XTAL_O
LSI_BYP_CLK	A21	LSI_BYP_CLK
LSI_BYP_EN	C17	LSI_BYP_EN
VSS	A22	VSS

VDD11_SYS

P6	VDD11_SYS
U7	VDD11_SYS
R7	VDD11_SYS
N7	VDD11_SYS
L7	VDD11_SYS
T8	VDD11_SYS
P8	VDD11_SYS
M8	VDD11_SYS
K8	VDD11_SYS
U9	VDD11_SYS
R9	VDD11_SYS
N9	VDD11_SYS
L9	VDD11_SYS
V10	VDD11_SYS
T10	VDD11_SYS
P10	VDD11_SYS
M10	VDD11_SYS
K10	VDD11_SYS
U11	VDD11_SYS
R11	VDD11_SYS
N11	VDD11_SYS
L11	VDD11_SYS
V12	VDD11_SYS
T12	VDD11_SYS
P12	VDD11_SYS
M12	VDD11_SYS
K12	VDD11_SYS
U13	VDD11_SYS
R13	VDD11_SYS
N13	VDD11_SYS
L13	VDD11_SYS
T14	VDD11_SYS
P14	VDD11_SYS
M14	VDD11_SYS
K14	VDD11_SYS
L15	VDD11_SYS
J15	VDD11_SYS
M16	VDD11_SYS
K16	VDD11_SYS

VDD33I00	J13	VDD33I00
VDD33I00	J14	VDD33I00
VDD33I00	G15	VDD33I00
VDD33I01	J10	VDD33I01
VDD33I01	J11	VDD33I01
VDD33I01	H10	VDD33I01
VDD33I02	H8	VDD33I02
VDD33I02	J7	VDD33I02
VDD33I03	G7	VDD33I03
VDD33I03	G5	VDD33I03
VDD33I04	F4	VDD33I04
VDD33I04	F2	VDD33I04
VDD33I05	V14	VDD33I05
VDD33I05	U14	VDD33I05
VDD33I05	W15	VDD33I05
VDD33I05	V15	VDD33I05
VDD33I05	U15	VDD33I05
VDD33I06	J16	VDD33I06
VDD33I06	H16	VDD33I06
VDD33I07	G17	VDD33I07
VDD33I07	E17	VDD33I07
VDD33_LDO	F15	VDD33_LDO
VDD33_LDO	G16	VDD33_LDO
VDD33_ANA	J8	VDD33_ANA
VDD33_FLASH	T15	VDD33_FLASH
VDD33_FLASH_PSO	P15	VDD33_FLASH_PSO
VDD33_FLASH_PSO	N15	VDD33_FLASH_PSO
VSS	U16	VSS
VSS	T7	VSS
VSS	P7	VSS
VSS	M7	VSS
VSS	K7	VSS
VSS	U8	VSS
VSS	R8	VSS
VSS	N8	VSS
VSS	L8	VSS
VSS	T9	VSS
VSS	P9	VSS
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VSS	K9	VSS
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VSS	L14	VSS
VSS	M15	VSS
VSS	K15	VSS
VSS	H15	VSS
VSS	F17	VSS
VSS	V18	VSS

